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IN THE CLAIMS

Applicant below submits a new complete claim set showing marked up claims with insertions indicated by underlining and deletions indicated by strikeouts and/or double bracketing.

Please amend claims 8, 16-20, 22 and 41 as shown.

1. (Previously amended) An analog-to-digital converter comprising:
a first section having a multi-bit analog to digital converter for receiving an analog input signal and generating an m-bit digital signal, and
an m-bit to n-bit converter (where $m > n$) for receiving the m-bit digital signal and for generating an n-bit digital output signal for outputting across an interface, wherein the m-bit to n-bit converter quantizes the m-bit signal to a lower resolution; and
a second section having processing means which is arranged to receive the n-bit digital signal and to process the received signal to generate an output digital signal.
2. (Original) An analog-to-digital converter according to claim 1 wherein the multi-bit analog to digital converter is a multi-bit sigma-delta modulator.
3. (Original) An analog-to-digital converter according to claim 1 wherein the m-bit to n-bit converter is an m-bit to single-bit converter.
4. (Original) An analog-to-digital converter according to claim 1 wherein the m-bit to n-bit converter receives the m-bit digital signal at substantially the same rate as it outputs the n-bit digital signal.